

Moriwaki et al.

FIG. 2A

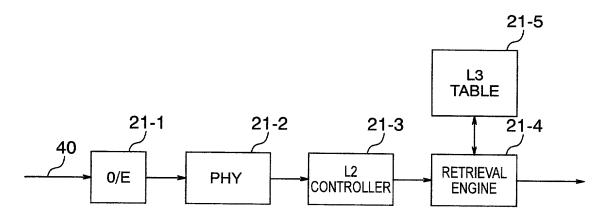
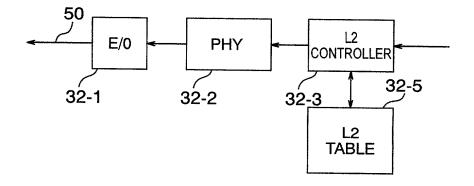
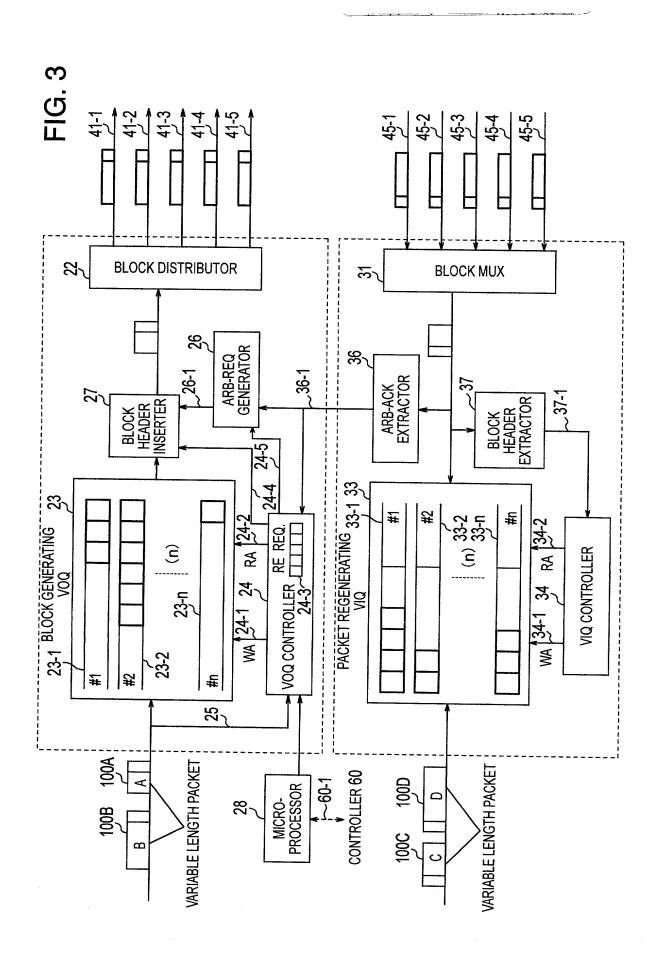


FIG. 2B





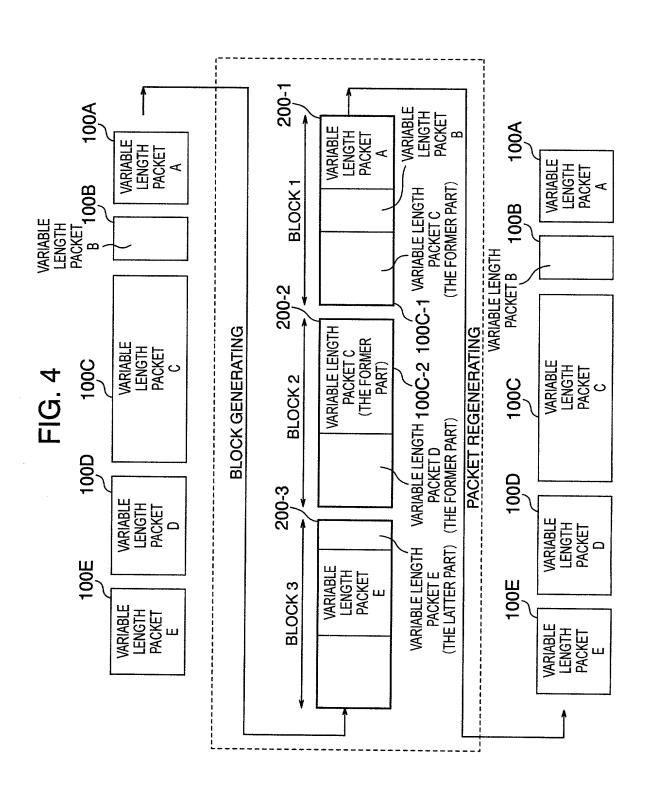


FIG. 5

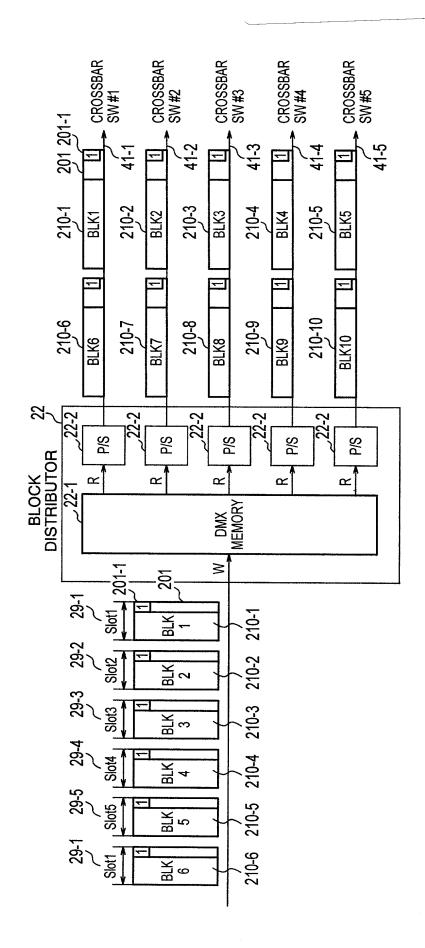
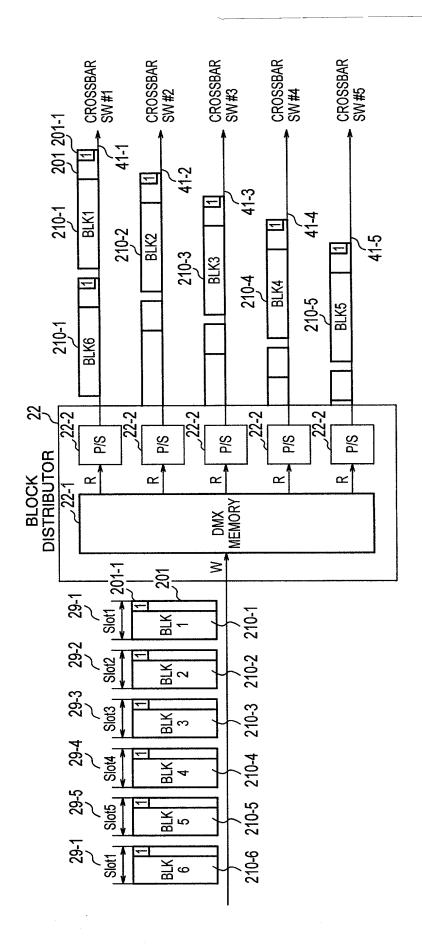


FIG. 6



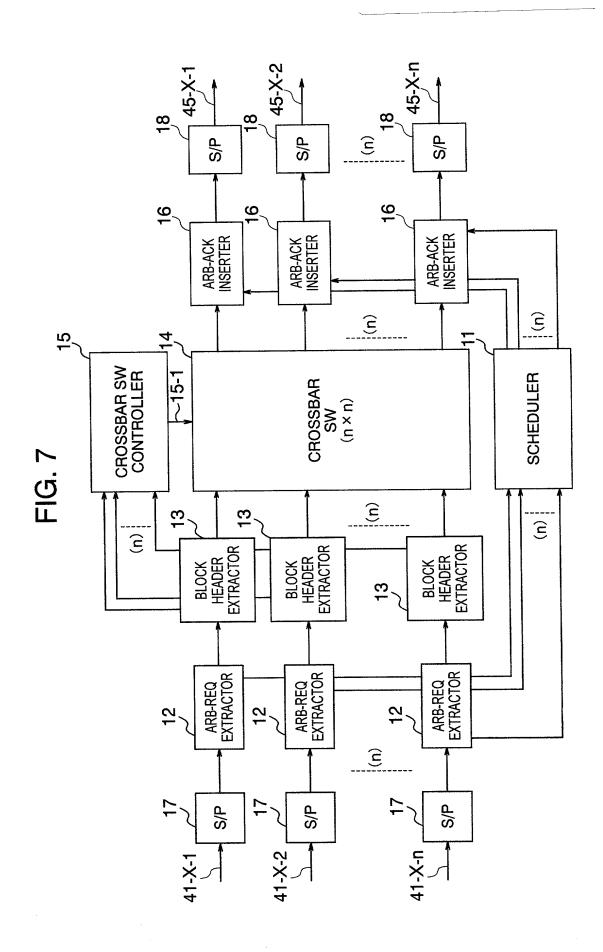
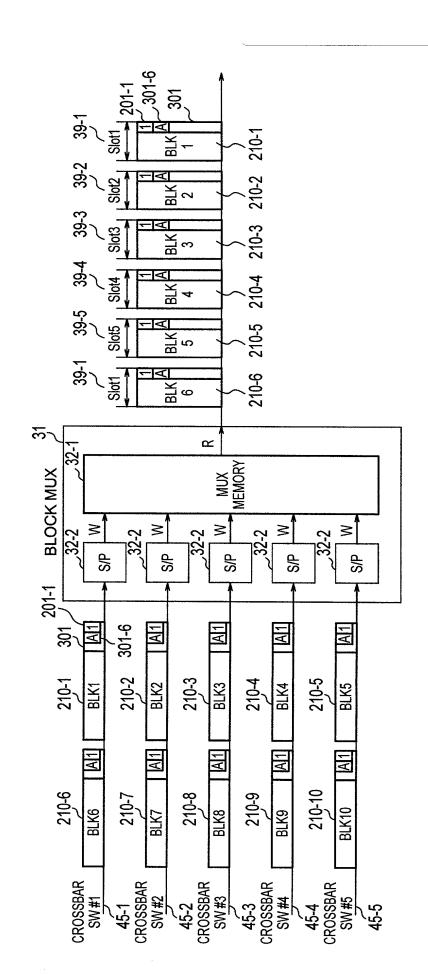


FIG. 8



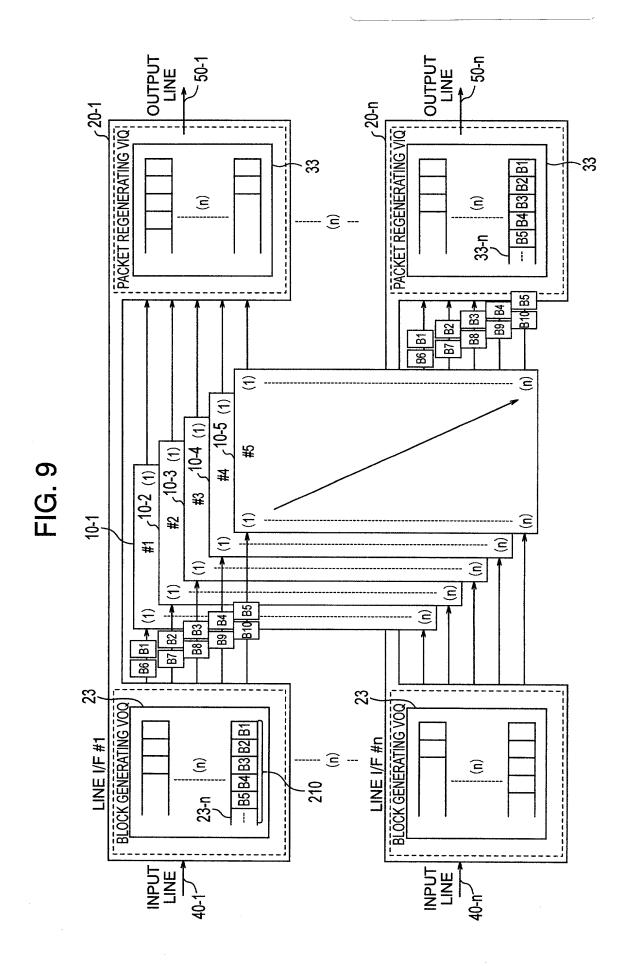


FIG. 10

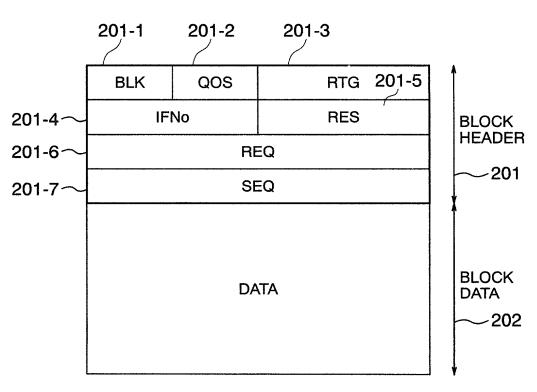


FIG. 11

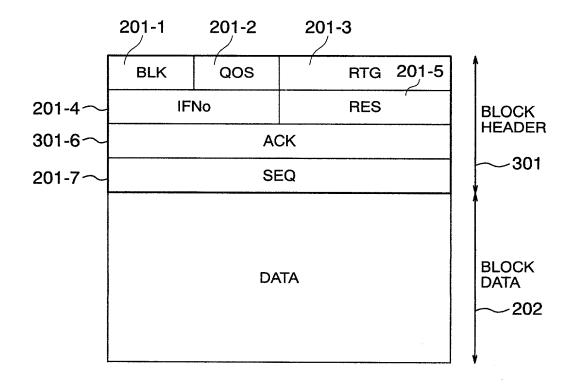
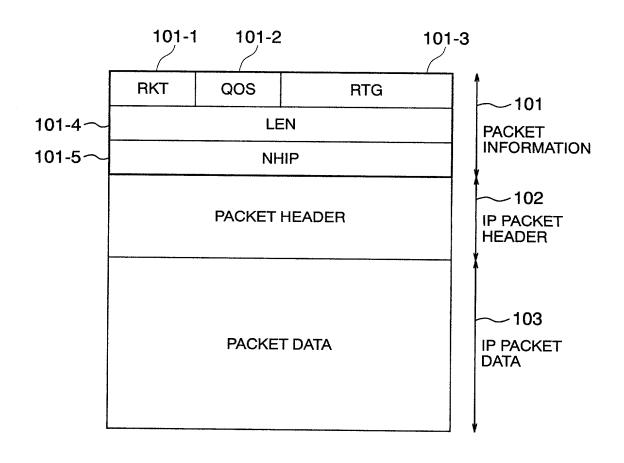


FIG. 12



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FIG. 13

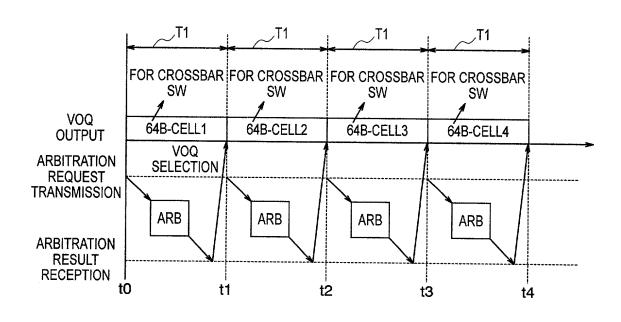


FIG. 14

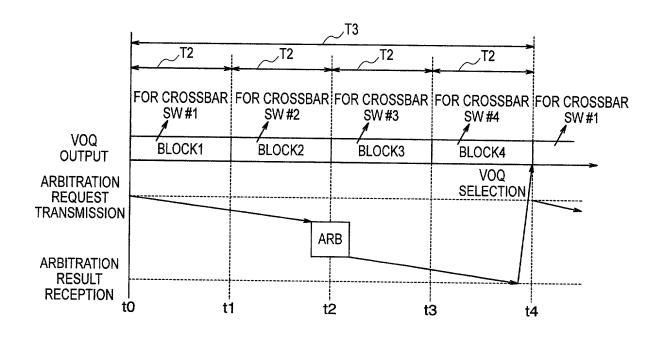
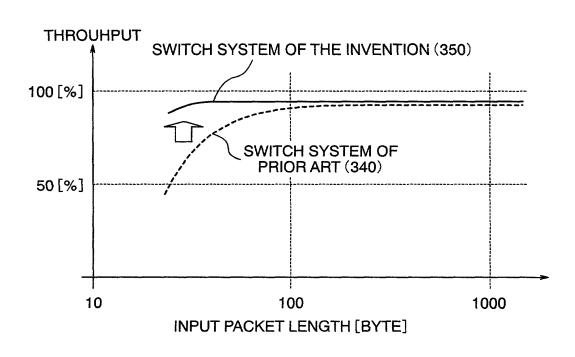


FIG. 15



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FIG. 16

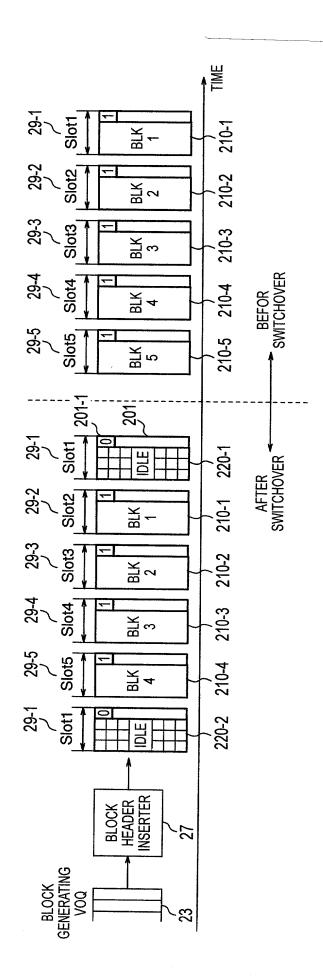
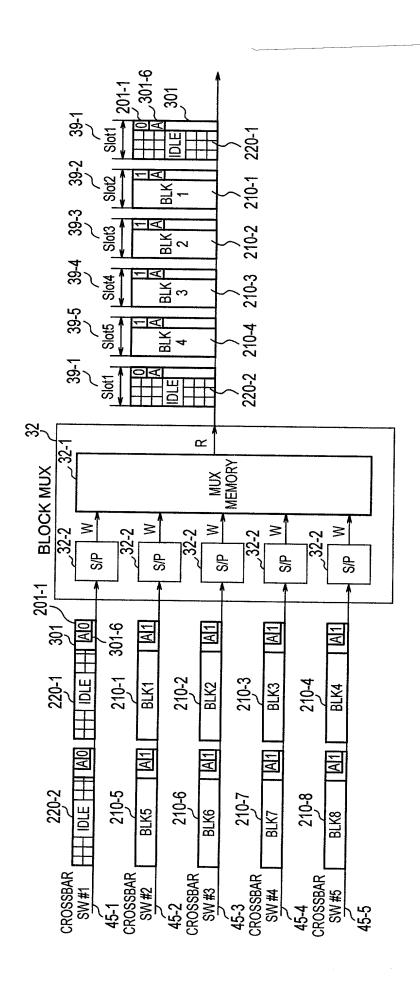
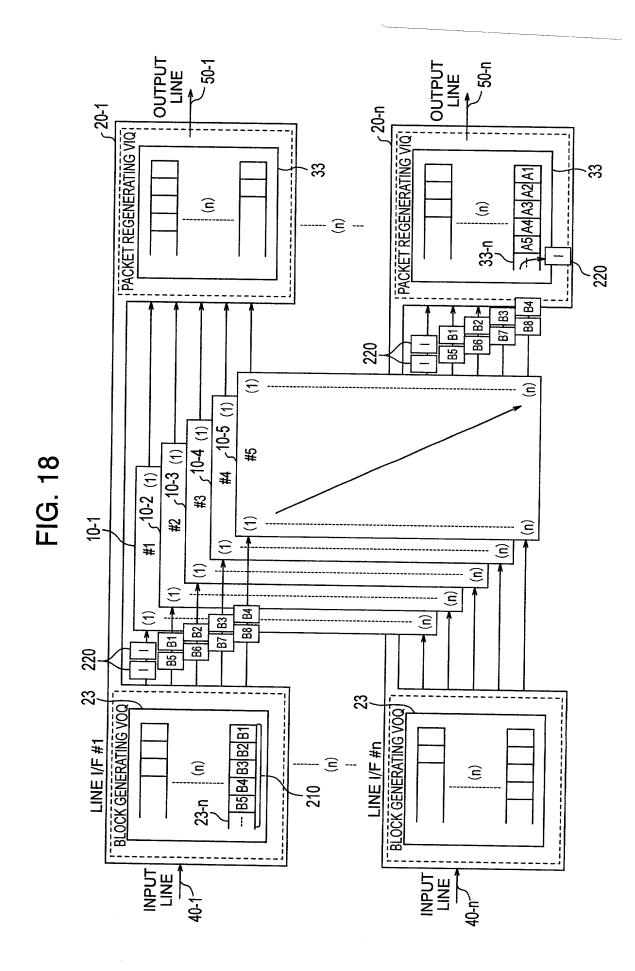


FIG. 17



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FIG. 19

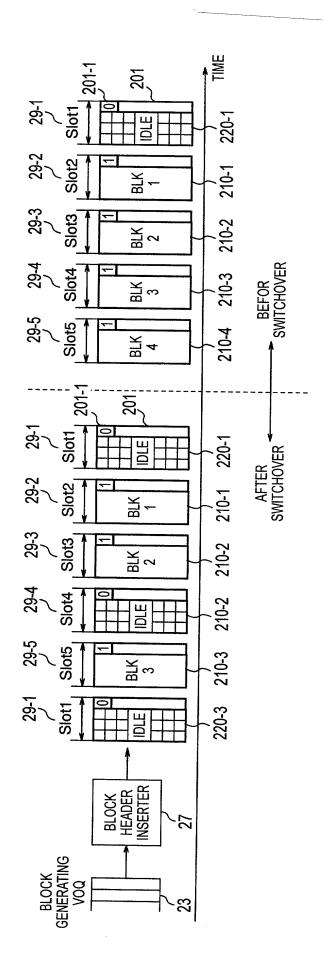
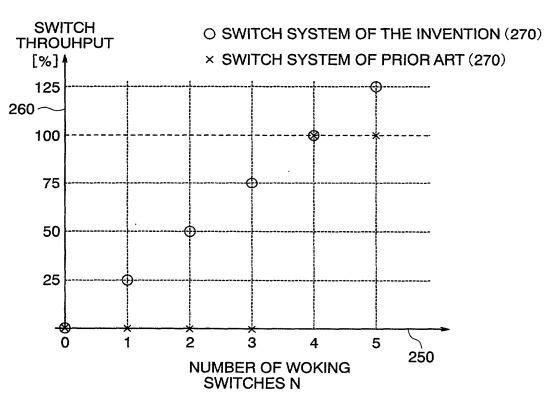


FIG. 20



(100% THROUGHPUT WHEN N=4)

FIG. 21

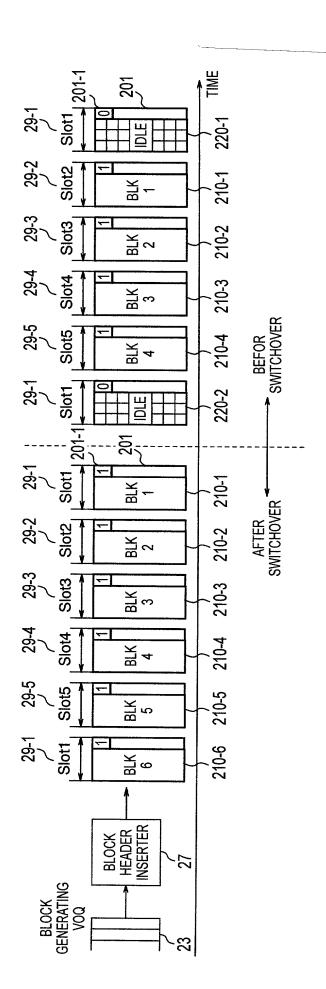
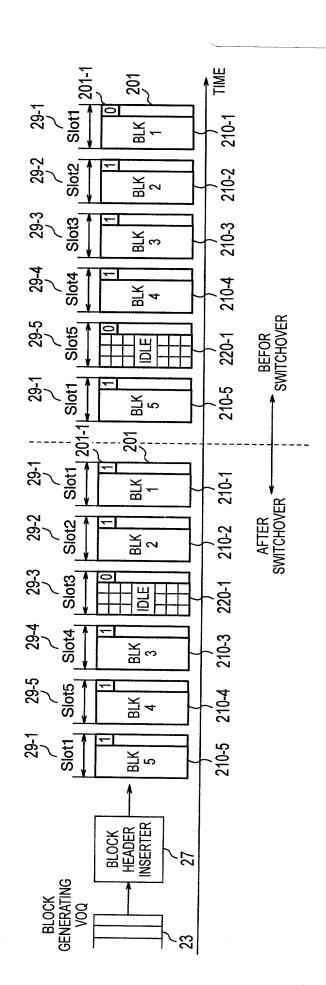


FIG. 22



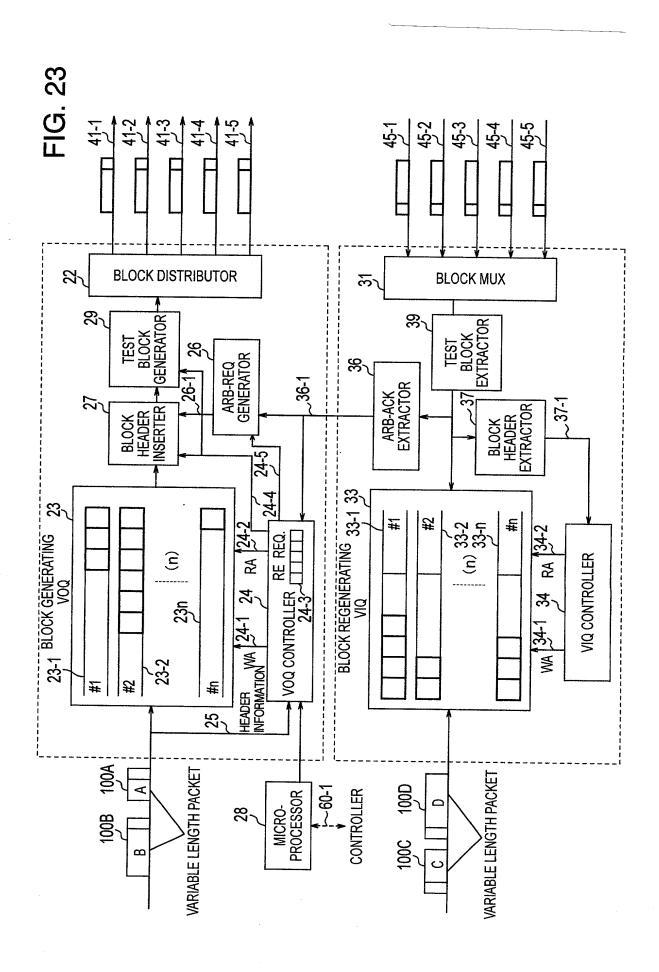


FIG. 24

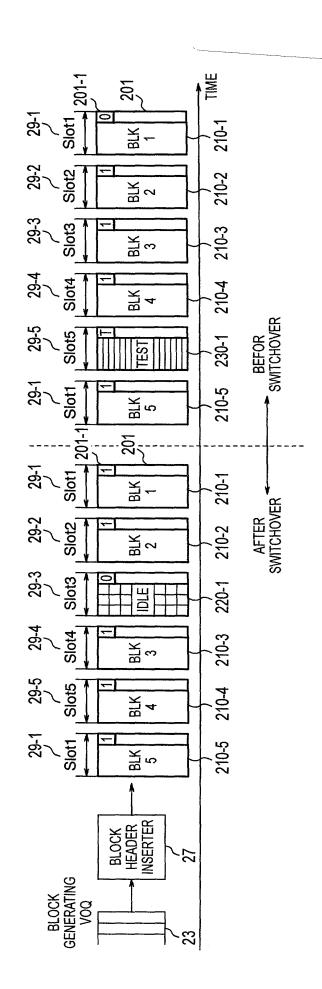


FIG. 25

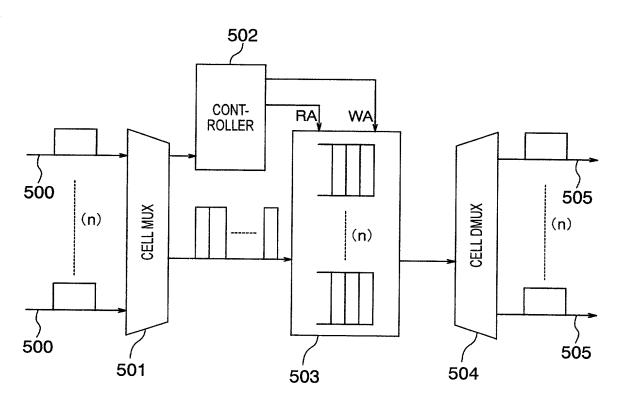


FIG. 26

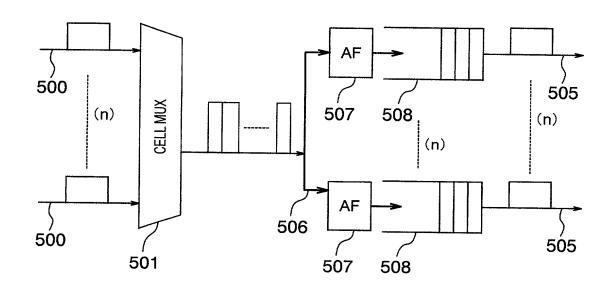
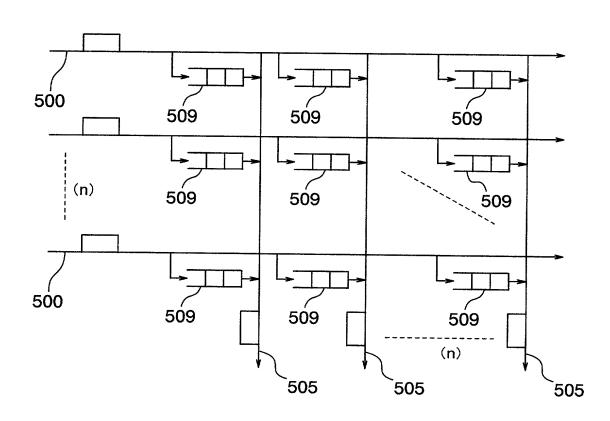
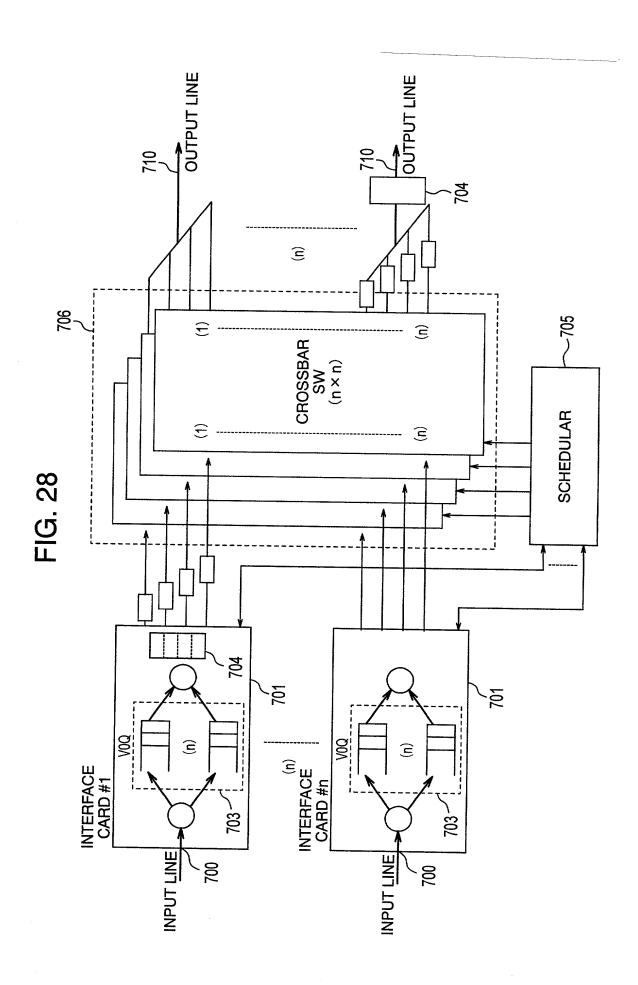


FIG. 27





24-3 VOQ CONTROLLER REREG FIG. 29 611 615 614 613 612 900 **CROSSBAR SWITCH 1**

